



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Fernando Gonzalez et al.

Serial No.: 10/751,141

Filed: December 31, 2003

For: Transistor Having Vertical Junction
Edge and Method of Manufacturing
the Same

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Group Art Unit: Unassigned

Examiner: Unassigned

Atty. Docket: MICS:0114 FLE/MAN
03-0027

Mail Stop Patent Application
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37 C.F.R. 1.8

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March 11, 2004

Date

Robert A. Marware

Sir:

**INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. §§ 1.97(b)(1) AND 1.98**

In compliance with the duty of disclosure under 37 C.F.R. § 1.56(a), Applicants respectfully request that this Information Disclosure Statement be entered and that the references listed on the PTO-1449 Form be considered by the Examiner and made of record. **In accordance with the revised procedures promulgated by the Office of Patent Legal Administration and relating to applications filed after June 30, 2003, copies of the listed references are not required to be submitted to the Examiner.**

In accordance with 37 C.F.R. § 1.97, this Information Disclosure Statement is not to be construed as a representation that a search has been made, as an admission that the information

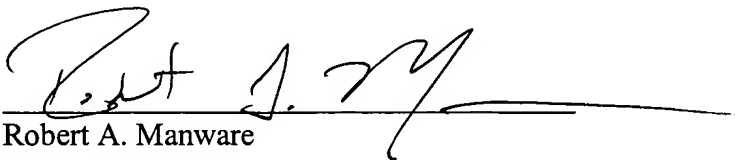
cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b), or as a representation that no other possibly material information, as defined in 37 C.F.R. § 1.56(b), exists.

Furthermore, the references listed on the attached PTO-1449 Form are not to be construed as an admission that these references qualify as prior art as to the above-referenced application or any related application. Rather, these references are being presented for the Examiner's consideration without prejudice to Applicants' right to demonstrate that any of these references do not qualify as prior art should the Examiner choose to apply any of these references.

The following information is listed below in accordance with 37 C.F.R. §1.98. Any explanation of non-English language documents contained in this Information Disclosure Statement is believed to constitute a concise explanation of the relevance of the listed reference as it is presently understood by the individual designated in § 1.56(c) most knowledgeable about the content of the listed reference, in accordance with 37 C.F.R. § 1.98(a)(3).

Respectfully submitted,

Date: March 11, 2004


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Form PTO-1449 (modified)	ATTY. DOCKET NO. MICS:0114 FLE/MAN (03-0027)	SERIAL NO. 10/751,141
List of Patents and Publications For Applicant's Information Disclosure Statement	APPLICANT Fernando Gonzalez et al.	
(Use several sheets if necessary)	FILING DATE December 31, 2003	GROUP

U.S. PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
		5,841,150	11-24-98	Gonzalez et al.	257	3	
		6,096,596	08-01-00	Gonzalez	438	239	
		6,118,135	09-12-00	Gonzalez et al.	257	3	
		6,429,104	08-06-02	Auberton-Herve	438	527	
		6,468,879	10-22-02	Lamure et al.	438	458	

FOREIGN PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION

OTHER ART

(Author, Title, Journal, Volume, Pertinent Pages, & Date)	
EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Information Disclosure Statement-PTO-1449 (Modified)